

CLAIM AMENDMENTS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended). A circuit configuration, comprising:

terminals for receiving at least two digital signals;

a calibration circuit connected to said terminals and having outputs outputting at least two digital output signals each derived from one of the digital signals, said calibration circuit ~~effects~~ effecting a temporal control of a switching edge of one of the two digital output signals using a control value, said calibration circuit containing a storage circuit for storing the control value; and

a comparison circuit connected to said outputs and having a terminal for a comparison signal indicating that one of the two digital output signals has a switching edge first relative to another of the two digital output ~~signal~~ signals;

said calibration circuit has a control input for setting  
~~through which~~ the control value ~~can be set~~ using a state of  
the comparison signal of said comparison circuit.

Claim 2 (original). The circuit configuration according to  
claim 1, wherein said terminal for the comparison signal of  
said comparison circuit is connected to said control input of  
said calibration circuit.

Claim 3 (original). The circuit configuration according to  
claim 1, wherein the comparison signal indicates with an  
active state that one of the two digital output signals has a  
switching edge first relative to the other of the two digital  
output signals, and said comparison circuit is embodied in  
such a way that the comparison signal has a periodic profile  
in the active state.

Claim 4 (original). The circuit configuration according to  
claim 3, wherein the two digital output signals have a  
periodic profile, and the comparison signal of said comparison  
circuit in the active state has a frequency of that output  
signal whose switching edges temporally precede corresponding  
switching edges of the other one of the two digital output  
signals.

Claim 5 (original). The circuit configuration according to claim 1, wherein said comparison circuit has a bistable multivibrator.

Claim 6 (original). The circuit configuration according to claim 1, wherein said calibration circuit has a circuit performing at least one of a filtering function and an integrating function and is connected to said terminal for the comparison signal of said comparison circuit.

Claim 7 (currently amended). The circuit configuration according to claim 1, wherein said calibration circuit contains a delay circuit having an input connected to one of said terminals for the digital signals, said delay circuit setting a delay of a switching edge of one of the digital signals, and said delay circuit has an ~~outputs~~ output connected to one of said outputs of said calibration circuit.

Claim 8 (original). The circuit configuration according to claim 7, wherein said delay circuit contains delay elements that can be set digitally.

Claim 9 (original). The circuit configuration according to claim 7, wherein said delay circuit contains delay elements that can be set in an analog manner.

Claim 10 (original). The circuit configuration according to claim 1, including an inverter circuit disposed and connected between said calibration circuit and said terminals.

Claim 11 (currently amended). A measuring device, comprising:

a circuit configuration, including:

terminals for receiving at least two digital signals;

a calibration circuit connected to said terminals and having outputs for at least two digital output signals each derived from one of the digital signals, said calibration circuit ~~effects~~ effecting temporal control of a switching edge of one of the two digital output signals using a control value, said calibration circuit containing a storage circuit for storing the control value; and

a comparison circuit connected to said outputs and having a terminal for a comparison signal which indicates that one of the two digital output signals has a switching edge first relative to another of the two digital output ~~signal~~ signals;

said calibration circuit ~~has~~ having a control input for  
setting ~~through which~~ the control value ~~can be set~~ using  
a state of the comparison signal of said comparison  
circuit.

Claim 12 (currently amended). A test device, comprising:

a circuit configuration, including:

terminals for receiving at least two digital signals;

a calibration circuit connected to said terminals and  
having outputs for at least two digital output signals  
each derived from one of the digital signals, said  
calibration circuit ~~effects~~ effecting temporal control of  
a switching edge of one of the two digital output signals  
using a control value, said calibration circuit  
containing a storage circuit for storing the control  
value; and

a comparison circuit connected to said outputs and having  
a terminal for a comparison signal which indicates that  
one of the two digital output signals has a switching

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edge first relative to another of the two digital output  
~~signal~~ signals;

said calibration circuit ~~has~~ having a control input for  
setting ~~through which~~ the control value ~~can be set~~ using  
a state of the comparison signal of said comparison  
circuit.